



501.37436CV2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: NISHIHARA et al.  
Application No.: 10/721,902  
Filed: November 26, 2003  
For: METHOD OF FABRICATING SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Art Unit: TBD  
Examiner: TBD

**SUPPLEMENTAL PRELIMINARY AMENDMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 13, 2004

Sir:

Supplementing the Preliminary Amendment filed November 26, 2003, please further amend the above-identified application, prior to examination thereof, as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.